

IN THE SPECIFICATION

Please correct the paragraph at page 1, lines 8-12, as follows:

This patent application is claiming priority under 35 USC § 121 to co-pending patent application entitled ON-CHIP INDUCTOR HAVING A SQUARE GEOMETRY AND HIGH Q
5 FACTOR AND METHOD OF MANUFACTURE THEREOF having a serial number of 10/074,158 and a filing date of ~~2/12/2002~~ February 12, 2002, now U.S. Patent No. 6,937,128.

Please correct the paragraph at page 18, lines 6-16, as follows:

~~A~~ A method of manufacturing an on-chip inductor and/or on-chip transformer includes
creating a ~~at least one~~ dielectric layer and creating a ~~at least one~~ conductive winding on the ~~at~~
10 ~~least one~~ dielectric layer. The conductive winding has a substantially square geometry and has at least its exterior corners geometrically shaped to reduce impedance of the conductive winding at a particular operating frequency. Since the quality factor of an on-chip inductor is inversely proportional to the effective series impedance of an inductor at an operating frequency, by reducing the effective series impedance, the quality factor is increased.

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